

CHIEF PATRONS

Dr. A. Prabhakar Reddy

President CBIT, Kolar.

Er. Krishna Reddy V

Secretary CBIT, Kolar.

PATRON

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Principal , CBIT, Kolar.

CONVENER

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ORGANIZING COMMITTEE

Prof. Sujani G

Prof. Sukumar B S

Prof. Eashwara M

Prof. Samson John

Prof. Jagadish Kumar G M

Prof. Kavya S

Prof. Sahana Patil

WORKSHOP CO-ORDINATORS

Prof. Kavya S,

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CBIT, Kolar.

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ABOUT THE INSTITUTE

C Byregowda Institute of Technology (CBIT), established in the year 2009 is approved by AICTE, New Delhi and is affiliated to Visvesvaraya technological University (VTU), Belagavi, Karnataka. The institute offers engineering UG courses in Electronics and Communication, Mechanical, Civil, Computer Science, Artificial Intelligence & machine learning and Computer Engineering branches. All the departments have well qualified staff and well-equipped laboratories, library, computing & sports facilities along with other amenities. The college is located at a distance of 63 kms from Bengaluru city.

The chief patrons of the college Dr. A. Prabhakar Reddy President of the Institute is great scholar and Doctor by profession and Er. Krishna Reddy. V Secretary of the Institute and Civil engineer and Dr. S.N Chandrashekara Principal of the college are supporting in all aspects and working for further upliftment of the rural college by allowing the Departments to conduct such workshops.

ABOUT THE DEPARTMENT

The Department of Electronics & Communication Engineering was established in the year 2009 with an initial intake of 60 and later enhanced to 120 in the year 2013. It aims at producing qualified Engineers in the area of Electronics.

Seminars and workshops are regularly organized to update students with latest Industry trends from reputed Organizations. The Department is having its own EC-FORUM "THE KITES"; where activities such as Circuit designing, Circuit troubleshooting, Encouraging research, conducting seminars on latest & The R&D Centre was established in the year 2015.



C Byregowda Institute of Technology

(Approved by AICTE New Delhi, Recognized by Govt. of Karnataka, Accredited by NAAC with B++ Grade, Affiliated to VTU Belagavi, ISO 9001:2015 Certified Institute)

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Five Days Workshop on

“Emerging Trends in VLSI and Allied Electronics Integrating with AI”

07-04-2025 to 09-04-2025 &

11-04-2025 to 12-04-2025

Organized by

Dept of ECE & R&D Centre

In Association with IQAC, IMAPS, IETE and Incubation Center



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REGISTRATION FORM

Name of the participant:

Dr/Mr/Ms/Mrs: _____

Designation: _____

Qualification: _____

Dept: _____

Name of the college / industry:

Address for communication (Mailing address):

Phone: _____

Email: _____

Signature
(Participant)

Principal /
Head of the Department

ABOUT THE WORKSHOP

This workshop explores emerging trends in VLSI, Embedded system, Mobile communication and allied electronics, focusing on their integration with Artificial Intelligence (AI). It aims to discuss cutting-edge innovations, AI-driven design, and smart electronic systems. Participants will gain insights into challenges and opportunities advancements in semiconductor technology, smart electronic systems, and the role of machine learning in optimizing electronic at this interdisciplinary intersection. The event bridges academia and industry, fostering collaboration and practical learning.

OBJECTIVE OF WORKSHOP

The workshop aims to provide an in-depth understanding of emerging trends in Very Large-Scale Integration (VLSI) and allied electronics, with a special focus on their integration with Artificial Intelligence (AI). It seeks to explore cutting-edge innovations, challenges, and applications in these domains, fostering interdisciplinary knowledge and encouraging collaboration among researchers, industry professionals, and students. Participants will gain insights into the transformative impact of AI on VLSI design and electronic systems, equipping them with the expertise to address real-world technological advancements.

WHO CAN ATTEND THIS WORKSHOP?

The Workshop has been specially designed for UG, PG Students, faculty members, Ph.D Scholars of various domains and R&D sectors. Participants can register for the Workshop through the given mail id by paying Rs 500

Email ID: hod.ece.cbit@gmail.com

Last date for online registration :05 - 04 - 2025

Confirmation of registration :05 - 04 - 2025

On spot registrations :07 - 04 - 2025

RESOURCE PERSONS

- ✓ **Dr. K D Nayak**, IMAPS.
- ✓ **Dr. M H Kori**, IMAPS.
- ✓ **Dr. Shankar Prasad**, IMAPS.
- ✓ **Mr. Sriikiran**, Director, Siplont Systems.
- ✓ **Ms. Chitra**, PD Engg., Siplont Systems.
- ✓ **Mr. Shiva**, Sales Manager, Siplont Systems.
- ✓ **Mrs. Amala**, Siplont Systems.
- ✓ **Dr. C V Ravishankar**, IETE.

TOPICS TO BE COVERED:

Day	Topic	Speakers
Day 1 7.4.25	<ul style="list-style-type: none">• 6G & Beyond• Overview of VLSI and Development• How to make carrier in VLSI and Job Prospects	(IMAPS) <ul style="list-style-type: none">• Dr. M H Kori• Dr. K D Nayak• Dr. Shankar K Prasad
Day 2 8.4.25	Cyber Security	(IETE) Dr. C V Ravishankar
Day 3 9.4.25	Embedded Systems	(STAR Company) <ul style="list-style-type: none">• Mr. Balaji• Mr. Sulthan
Day 4 11.4.25	VLSI Basics to Advancement	(Siplont Systems) <ul style="list-style-type: none">• Mr. Sriikiran Director• Mr. Shiva Sales Manager• Ms. Chitra• Mrs. Amala
Day 5 12.4.25	Industrial Visit	

 **Venue:**

**Seminar Hall,
C Byregowda Institute of Technology,
Srinivasapura Road, Kolar. 563101**

