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Dr. D Hemachandra Sagar, Chairman, DSI,Bengaluru
Dr. D Premachandra Sagar, Vice-Chairman, DSI ,Bengaluru

Patrons

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Mrs Tintisha H Sagar, Joint Secretary, DSI,Bengaluru
Mr. Rohan Prem Sagar, Joint Secretary, DSI ,Bengaluru
Mr.Nishan H Sagar, Joint Secretary, DSI ,Bengaluru
Ms.Natasha H Sagar, Joint Secretary, DSI,Bengaluru

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Dr. B G Prasad,Principal, DSCE,Bengaluru

Convenor

Dr. Shobha K R, Professor & Head
Dept. of ECE, Dean IQAC, DSCE, Bengaluru

Advisory Committee

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Dr. Ramaraju H K, Vice Principal, DSCE, Bengaluru
Dr. Suma V, Vice Principal, DSCE, Bengaluru
Dr. A R Aswatha, Dean Student Affairs, DSCE, Bengaluru
Dr. Annapurna P Patil, Dean Academics, DSCE, Bengaluru
Dr. Sriraam N, Dean R&D, DSCE, Bengaluru

Coordinators

Dr Sreenath Settur,Professor of Practice, Dept of ECE. DSCE
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Dr.Dinesha P, Professor, Dept of ECE ,DSCE
Email:drdinesh-ece@dayanandasagar.edu

Co - Coordinators

Dr. Shashi Raj K, Assistant Professor
Dr. Basavaraj S.K, Assistant Professor
Dr.Madhura R, Assistant Professor

Organizing Secretaries

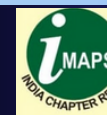
Dr. Kiran A. Gupta, Professor
Dr. Jamuna S, Professor
Dr .P.Vimala, Professor

Organizing Committee

Dr. Vinay N.A, Assistant Professor
Mrs. Chaitra .A, Assistant Professor
Mrs .Bindu H.M, Assistant Professor
Mrs. Padmavathi M, Assistant Professor
Mrs. Shruthi .M.S, Assistant Professor
Mrs. Ashwini Gowda H.B, Assistant Professor

Invited Speakers

Dr. K.D. Nayak, Former DG, DRDO,IMAPS. Bengaluru
Dr. M.H. Kori, Former Technical Director, Alcatel
Lucent, IMAPS, Bengaluru
Dr. Ravi Meduri, Director, STARC,IMAPS, Bengaluru
Dr. Shankara Prasad, Professor of Practice, IIT,
Guwahati
Dr. Y.V.S Lakshmi, Advisor IPR , C-DOT, Bengaluru
Dr. G.R Joshi, Former Scientist , URRC, ISRO,
Bengaluru
Dr. Veena Chakravarti, Senior Professor, Bengaluru
Mr B. Bhattacharya, DGM, STARC, Bengaluru
Mr. Jayaramu, Manager, STARC, Bengaluru
Dr. Shalini Shrivastava, Visiting Research Fellow ,IISc,
Bengaluru
Dr .Sabiha, Chief Technologist,CeNSE,IISC, Bengaluru
Mr.Sharat Kaul,Semiconductor Expert,
Consultant,Kaynes Semicon, Bengaluru

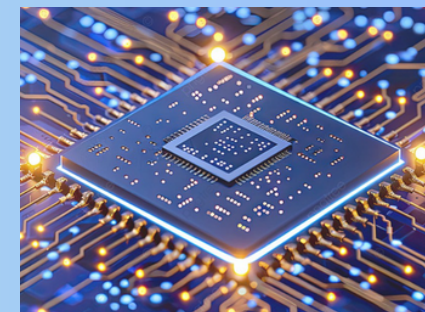


Dayananda Sagar College of Engineering

An Autonomous Institute affiliated to VTU,
Approved by AICTE & UGC, NAAC with A grade,
Shavige Malleshwara Hills, Kumaraswamy Layout, Bengaluru -
560111

5 DAYS FACULTY DEVELOPMENT PROGRAM ON

**A Learning on Complete Life Cycle of
VLSI Chip Making
10th- 14th , March 2025**



Organised by



DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Venue: Library AV Auditorium,
Building No. 2, DSCE
Bengaluru - 560111

About DSCE

Dayananda Sagar College of Engineering (DSCE), founded in 1979 under the Mahatma Gandhi Vidya Peetha Trust is a premier technical institution in India, affiliated with Visvesvaraya Technological University (VTU) and approved by AICTE, spread across a 23-acre campus. DSCE offers 20 undergraduate, 6 postgraduate engineering programs, and supported by 18 VTU-approved research centers. The college boasts modern infrastructure, Wi-Fi-enabled facilities, and extensive library resources. It has earned an 'A' Grade NAAC accreditation, NBA accreditation for 12 programs, and high national rankings, including an NIRF Innovation rank (201 - 300). DSCE emphasizes research and innovation with several R&D projects sponsored by prestigious organizations like ISRO and AICTE. The college fosters entrepreneurship through its IEDC and equips students with industry relevant skills via Centers of Excellence and Leadership Programs. Its Placement Division supports career readiness, securing internships and placements for students, while a strong alumni network connects graduates to the institution and each other

About FDP

This Faculty Development Program is designed to set a constructive platform for educators, and researchers to enhance their knowledge, teaching methodologies, and research capabilities in this rapidly evolving field. This pioneering initiative aims to address India's growing demand for skilled professionals in semiconductor fabrication and packaging. While Indian engineers excel in VLSI design, expertise in fabrication and packaging remains a challenge.. With the Indian government emphasizing semiconductor manufacturing and investing nearly Rs 2 lakh crores has indicated that there is a pressing need for 30,000+ trained professionals. This FDP aims to bridge this gap, fostering advanced learning and skill development in this crucial domain.

About Department

The Department of Electronics and Communication Engineering at DSCE is one of the oldest and largest, with 50 faculty members and over 1,200 students. Among the faculty, 31 hold Ph.D.s, while the rest are pursuing their doctorates, ensuring high-quality technical education at undergraduate, postgraduate, and research levels.

The department offers a B.E. program, an M.Tech in VLSI Design and Embedded Systems, and Ph.D. opportunities. With state-of-the-art infrastructure, advanced labs, and a strong focus on R&D, it emphasizes cutting-edge research and industry readiness.

Students gain hands-on experience through value-added courses, skill development programs, hackathons, and exposure to emerging technologies via professional societies and technical clubs. The dedicated faculty also support students in securing prestigious placements worldwide

Registration Details

Last date for registration -7th March 2025.

Registration Fee:

DSCE Faculty/Students – Free
External Faculty – Rs. 1000/-
External Scholars – Rs. 500/-
Industry Participants – Rs. 1500/-
IMAPS Members – Rs. 500/-

Registration Link :

<https://forms.gle/P21aiPbQpfErrqGM7>

Target Audience

- Faculty
- PhD Scholars
- M.Tech Students.
- Industry
- IMAPS Members

About IMAPS

The International Microelectronics & Packaging Society, founded in 1967, is the largest society dedicated to the advancement and growth of microelectronics and electronic packaging. The IMAPS – has around 70 chapters around the globe, 8000+ members throughout the world including USA, Europe and Asia. IMAPS India was founded in 1985 under the guidance of Prof. B.S. Sonde. IMAPS India is one of the very active chapters of IMAPS International, serving the microelectronic community of India. IMAPS India has successfully conducted three International Conferences EMIT-96, EMIT-98 & EMIT2K and several National Conferences including Conferences at IIT Bombay, NRSC Hyderabad, BMSCE Bangalore, SIT Tumkur, Gopalan Institute, Bengaluru etc.

Objectives of FDP

- Enhance Faculty Expertise
- Bridge the Skill Gap
- Promote Industry-Academia Collaboration
- Support India's Semiconductor Mission
- Encourage Research and Innovation
- Prepare for Future Workforce Needs

Payment Details

A/c. No. : 74370100017988

IFSC code : BARB0VJDASC

Beneficiary Name: DSCE Function & Seminar

UPI ID :dsces90350@barodampay



Number of Seats: 60 max.

Contact Details

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